APPENDIX

APPENDIX A

<u>ACRONYMS</u>

APL **Approved Parts List**

Automatic Test Equipment ATE

BIT **Built-In Test**

Computer-Aided Design

CAI CA C Computer-Aided Manufacturing

CD Critical Design Review

CN Z Complimentary Metal Oxide Semiconductor ---

Department of Defense Da Destructive Physical Analysis DPA

Defense Systems Acquisition Review Council DSARC

Defense Science Board DSB DT **Development Test**

DT&E **Development Test and Evaluation**

Engineering Change Proposal ECP ESS Environmental Stress Screening

FOT&E Follow-on Operational Test and Evaluation

Failure Reporting, Analysis, and Corrective Action System FRACAS

FSD Full-Scale Development

GAO **General Accounting Office**

Institute of Environmental Sciences **IES**

ILS **Integrated Logistics Support**

Industrial Modernization Incentive Program IMIP initial Operationat Test and Evaluation IOT&E

IPF Initial Production Funds

ISD Instructional Systems Development

ITP Integrated Test Plan

Justification for Major System New Start **JMSNS**

LOL Loss of Learning

LSA Logistics Support Analysis

MFHBF Mean Flight Hours Between Failure

MTBF Mean Time Between Failure

MIL-HDBK Military Handbook
MIL-SPEC Military Specification
Military Standard

NOSC Naval Ocean Systems Center

NWC Naval Weapons Center

OSD Office of the **Secretary** of Defense

OT Operational Test

PAT Production Acceptance Test

PCB Printed Circuit Board
PIN Particle Induced Noise

PPBS Planning, Programming, and Budgeting System

PRR Production Readiness Review

RDT&E Research, Development, Test, and Evaluation

RFP Request for Proposal

s o w Statement of Work STE Special Test Equipment

TAAF Test, Analyze and Fix T&E Test and Evaluation

TEMP Test and Evaluation Master Plan

TPS Test Program Set

TTL Transistor/Transistor Logic

USDR&E Under Secretary of Defense for Research and Engineering